

DRIVING ALL N-CHANNEL POWER FETs WITH THE LS7261/LS7262 BRUSHLESS DC MOTOR COMMUTATOR/CONTROLLER

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Using a 12V and a 24V supply, Direct Drive of all N-Channel FETs by the LS7261/LS7262 can be achieved. If only a 12V power supply is available, a 555 Timer operating in the astable mode can be used to generate the 24V supply, as indicated in the figure. By connecting Pin 5 (Common) and Pin 11 (Vss) of the IC to 24V, the gates of the upper N-Channel Power FETs will be driven to 24V. Since the drains are tied to 12V, the Upper N-Channel

FETs will provide motor current at a very low VDS. The gates of the lower N-Channel FETs are also driven to 24V. If 24V Gate to Source is too high for the lower FETs, series resistors can be added between the Outputs and the Gates to divide down the 24V.

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